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**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = SMITH

First Name = STERLING

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09232304</a>	Not Issued	149	01/15/1999	SOFT GAIN UPDATE METHOD AND APPARATUS	SMITH, STERLING
<a href="#">09419616</a>	Not Issued	161	10/14/1999	METHOD AND APPARATUS FOR HIGH SPEED ANALOG TO DIGITAL CONVERSION OF VIDEO GRAPHIC SIGNALS USING INTERLEAVING	SMITH, STERLING
<a href="#">10139689</a>	<a href="#">6768385</a>	150	05/02/2002	INTELLIGENT PHASE LOCK LOOP	SMITH, STERLING
<a href="#">10144413</a>	<a href="#">6714084</a>	150	05/09/2002	HIGH RESOLUTION DIGITAL CONTROLLED OSCILLATOR	SMITH, STERLING
<a href="#">10262872</a>	Not Issued	161	10/03/2002	Anti-fuse sense amplifier	SMITH, STERLING
<a href="#">10409125</a>	<a href="#">6841981</a>	150	04/09/2003	RADIO FREQUENCY DATA COMMUNICATION DEVICE IN CMOS PROCESS	SMITH, STERLING
<a href="#">10615845</a>	Not Issued	51	07/10/2003	Digital spread spectrum frequency synthesizer	SMITH, STERLING
<a href="#">10615992</a>	<a href="#">6844758</a>	150	07/10/2003	FREQUENCY SYNTHESIZER	SMITH, STERLING
<a href="#">10619488</a>	Not Issued	30	07/16/2003	Digital frequency synthesizer based PLL	SMITH, STERLING
<a href="#">10624548</a>	<a href="#">7095288</a>	150	07/23/2003	LOGIC SYSTEM WITH ADAPTIVE SUPPLY VOLTAGE CONTROL	SMITH, STERLING
<a href="#">10663719</a>	<a href="#">7098824</a>	150	09/17/2003	APPARATUS AND METHOD FOR DITHERING IMAGE SIGNALS IN COMBINATION WITH OFFSET	SMITH, STERLING
<a href="#">10663755</a>	Not Issued	41	09/17/2003	Interface circuitry for display chip	SMITH, STERLING

<u>10698623</u>	Not Issued	30	11/03/2003	Data recovery circuit, phase detection circuit and method for detecting and correcting phase conditions	SMITH, STERLING
<u>10699649</u>	6895542	150	11/04/2003	DATA RECOVERY CIRCUIT AND METHOD AND DATA RECEIVING SYSTEM USING THE SAME	SMITH, STERLING
<u>10711175</u>	7026878	150	08/30/2004	FLEXIBLE SYNTHESIZER FOR MULTIPLYING A CLOCK BY A RATIONAL NUMBER	SMITH, STERLING
<u>10904159</u>	7106231	150	10/27/2004	VIDEO SIGNAL PROCESSING SYSTEM INCLUDING ANALOG TO DIGITAL CONVERTER AND RELATED METHOD FOR CALIBRATING ANALOG TO DIGITAL CONVERTER	SMITH, STERLING
<u>10904269</u>	Not Issued	71	11/02/2004	CLOCK GENERATOR AND RELATED BIASING CIRCUIT	SMITH, STERLING
<u>10908473</u>	Not Issued	41	05/13/2005	METHOD OF FRAME SYNCHRONIZATION WHEN SCALING VIDEO AND VIDEO SCALING APPARATUS THEREOF	SMITH, STERLING
<u>10908741</u>	7084795	150	05/24/2005	VIDEO SIGNAL PROCESSING SYSTEM WITH A DYNAMIC ADC CALIBRATION LOOP AND RELATED METHODS	SMITH, STERLING
<u>10908743</u>	Not Issued	30	05/24/2005	High-Speed Video Signal Processing System	SMITH, STERLING
<u>11001121</u>	Not Issued	30	12/02/2004	AM data recovery circuit	SMITH, STERLING
<u>11001133</u>	Not Issued	41	12/02/2004	AM DATA RECOVERY CIRCUIT	SMITH, STERLING
<u>11002618</u>	6999327	150	12/03/2004	CHARGE PUMP FOR RADIO FREQUENCY DATA COMMUNICATION DEVICE	SMITH, STERLING
<u>11103532</u>	Not Issued	30	04/12/2005	Apparatus and method for increasing pixel resolution of image using coherent sampling	SMITH, STERLING
<u>11163127</u>	Not Issued	30	10/06/2005	ADC SYSTEM, VIDEO DECODER AND RELATED METHOD FOR DECODING COMPOSITE VIDEO SIGNAL UTILIZING CLOCK	SMITH, STERLING

				SYNCHRONIZED TO SUBCARRIER OF COMPOSITE VIDEO SIGNAL	
<u>11163153</u>	Not Issued	20	10/06/2005	APPARATUS AND RELATED METHOD FOR LEVEL CLAMPING CONTROL	SMITH, STERLING
<u>11163717</u>	Not Issued	30	10/28/2005	AUTOMATIC GAIN CONTROL ANALOG-TO-DIGITAL CONVERTING SYSTEM AND RELATED METHOD	SMITH, STERLING
<u>11307219</u>	Not Issued	25	01/27/2006	APPARATUS AND METHOD FOR BATTERYLESS HIERARCHY REMOTE CONTROL	SMITH, STERLING
<u>11423149</u>	Not Issued	20	06/09/2006	Flat panel display device, Controller, and Method For Displaying Images	SMITH, STERLING
<u>11425719</u>	Not Issued	20	06/22/2006	Flat Panel Display Device, Controller, and Method for Displaying Images	SMITH, STERLING
<u>60071564</u>	Not Issued	159	01/15/1998	SOFT GAIN UPDATE METHOD AND APPARATUS	SMITH, STERLING
<u>60290473</u>	Not Issued	159	05/11/2001	High resolution digital controlled oscillator	SMITH, STERLING
<u>60290475</u>	Not Issued	159	05/11/2001	Intelligent phase lock loop	SMITH, STERLING
<u>60371363</u>	Not Issued	159	04/09/2002	Radio frequency data communication device in CMOS process	SMITH, STERLING
<u>60389893</u>	Not Issued	159	06/20/2002	Static CMOS anti-fuse sense amplifier	SMITH, STERLING
<u>60395338</u>	Not Issued	159	07/12/2002	Digital spread spectrum frequency synthesizer	SMITH, STERLING
<u>60395339</u>	Not Issued	159	07/12/2002	Digital frequency synthesizer	SMITH, STERLING
<u>60396900</u>	Not Issued	159	07/18/2002	Digital frequency synthesizer based PLL	SMITH, STERLING
<u>60398613</u>	Not Issued	159	07/26/2002	Adaptive supply voltage control for low power logic	SMITH, STERLING
<u>60412791</u>	Not Issued	159	09/24/2002	Clamping circuit integrated with filter	SMITH, STERLING
<u>60412792</u>	Not Issued	159	09/24/2002	Apparatus and method for masking interference noise contained in signal source	SMITH, STERLING

<a href="#">60423390</a>	Not Issued	159	11/04/2002	Apparatus and method of data recovery and cycle alignment	SMITH, STERLING
<a href="#">60423392</a>	Not Issued	159	11/04/2002	Apparatus and method of high speed clock and data recovery	SMITH, STERLING
<a href="#">60495710</a>	Not Issued	159	08/18/2003	Frame-bufferless response time enhancement (RTE) for LCD display panel	SMITH, STERLING
<a href="#">60498697</a>	Not Issued	159	08/29/2003	Flexible digital synthesizer for multiplying a clock by a rational number formed from large integers	SMITH, STERLING
<a href="#">60516763</a>	Not Issued	159	11/04/2003	Calibrated analog to digital conversion system	SMITH, STERLING
<a href="#">60517076</a>	Not Issued	159	11/05/2003	CMOS oscillator and bias circuit	SMITH, STERLING
<a href="#">60567786</a>	Not Issued	159	05/05/2004	Apparatus and method for increasing the pixel resolution of an image	SMITH, STERLING
<a href="#">60571514</a>	Not Issued	159	05/17/2004	Apparatus and method for adjusting the size of an image	SMITH, STERLING
<a href="#">60576022</a>	Not Issued	159	06/02/2004	Gain and offset calibration method for 10-bit ADC	SMITH, STERLING

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